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Y					Application Number	10/619,828	
1					Filing Date	07/15/2003	
- [S	TATEMENT !	BY APPLICA	ANT	First Named Inventor	Sheu, et al.	
					Art Unit	2829	
L	(Use as many sheets as necessary)				Examiner Name	Dana Farahani	
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DF	1	WOLF, S., et al., "Silicon Processing For The VLSI Era," Volume 1: Process Technology, Second Edition, Lattice Press, Sunset Beach, California, 2000, pp. 834-835.	
OF	2	WOLF, S., "Silicon Processing For The VLSI Era," Volume 2: Process Integration, Lattice Press, Sunset Beach, California, 1990, pp. 144-145.	
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